

PRELIMINARY AMENDMENT

Serial Number: Unknown

Filing Date: Herewith

Title: PIPELINED PACKET-ORIENTED MEMORY SYSTEM HAVING A UNIDIRECTIONAL COMMAND AND ADDRESS BUS AND A BIDIRECTIONAL DATA BUS

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Dkt: 303.306US2

CONCLUSION

Claims 1-4 and 9-28 have been canceled without prejudice and not in response to an art rejection. Applicant reserves the right to reintroduce these claims at a later date or in a continuation or divisional patent application. Claims 5, 6, and 29-31 have not been amended in response to any art rejection. New claims 32-56 are drawn to control of pipelined memories and fit within Group II of the Restriction Requirement of the parent case.

Claims 5-8 and 29-56 are now pending in this application. The Examiner is invited to contact the below-signed attorney to discuss any questions which may remain with respect to the present application.

Respectfully submitted,

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By their Representatives,

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